



Attorney's Docket No.: 10559-824001/P15891
Intel Corporation

IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Larry R. Tate et al.

Art Unit: 2631

Serial No.: 10/629,325

Examiner:

Filed : July 28, 2003

Assignee : Intel Corporation

Title : SIGNALING WITH MULTIPLE CLOCKS

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached International Search Report dated December 15, 2004, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 15, 2005

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communication from a foreign patent office in a counterpart foreign application.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

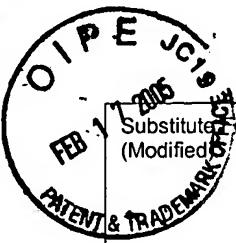
Date: February 14, 2005

for

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Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-824001Application No.
10/629,325**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR § 1.98(b))

Applicant
Larry R. Tate et al.Filing Date
July 28, 2003Group Art Unit
2631**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,785,394	11/1988	Fischer			
	AB	6,463,092	10/2002	Kim, et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AL	0 596 694	05/1994	Europe			
	AM						
	AN						
	AO						
	AP						

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	Nogami, Kazutaka, et al. "Phase Modulation I/O Interface Circuit", IEEE International Solid-State Circuits Conference, New York, USA, vol. 37, pp. 108-109, 318, February (1994).
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.